



Call Technisys for:

- Engineering/Testing
- Product Development
- Documentation
- PCB Design
- CPLD/FPGA Development
- Re-Engineering

PN701237

Features:

12 LAYER PRINTED CIRCUIT BOARD

HIGH DENSITY SMALL FOOT PRINT SURFACE MOUNT DEVICES

SUPPORTS FOUR OPERATING VOLTAGES

11 I/O CONNECTORS

STM-1 TO E4 AND E3 INTERFACES

155Mbps AND 139Mbps SERIAL DATA LINES

38.88MHz - 4 BIT PARALLEL DATA BUS

6 mil ROUTS WITH 50 OHM CONTROLLED IMPEDANCE

HIGH DENSITY 668 PIN BALL GRID ARRAY (BGA) FPGA DESIGN

CAN STORE AND LOAD 1 TO 7 APPLICATION FILES ON A SINGLE COMPACT FLASH CARD

REMOVABLE OPTICAL TRANSCEIVER (SFP)

DIFFERENTIAL PAIR ROUTING

ALL BOARDS ARE DESIGNED WITH CADENCE ALLEGRO SOFTWARE

BOARD DESIGNS WITH DATA RATES UP TO 10Gbs

Over 12 years providing electronic product development and hardware to NSA

Innovative Electronic Engineering from Concept Through Production

